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5-13-2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Michael Golanbari, et al.

Docket No: TI-33275

Serial No: 10/057,012

Conf. No: 3589

Examiner: TBD

Art Unit: 2631

Filed: 01/25/02

For: SHORT AND LONG SEQUENCE BOUNDARY DETECTION ALGORITHM FOR
WIRELESS LAN MODEMS

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Technology Center 2600

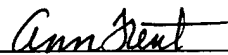
LETTER TO OFFICIAL DRAFTSPERSON

Assistant Commissioner For Patents
Washington, DC 20231

Attention: Official Draftsperson

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

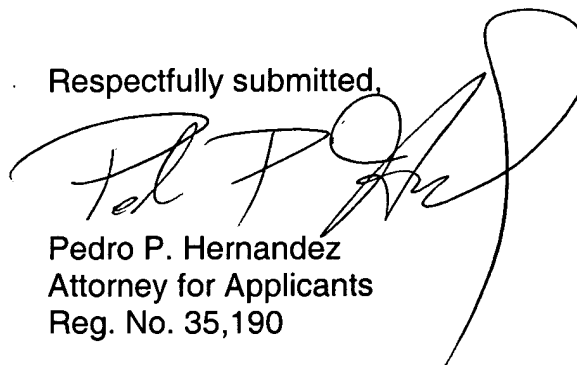
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on 3-19-02.


Ann Trent

Dear Sir:

Transmitted herewith for filing are two sheets of formal drawings for the above identified application. Charge any necessary fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



Pedro P. Hernandez
Attorney for Applicants
Reg. No. 35,190

Texas Instruments Incorporated
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